

In the claims:

For the Examiner's convenience, all pending claims are presented below with changes shown in accordance with the new mandatory amendment format.

1. (Currently Amended) A method for updating ACPI machine language (AML) code, comprising:

searching for a pointer to a starting address of the AML code;

providing appropriate update values for the AML code corresponding to board capabilities determined by using General Purpose Input Output (GPIO) pins; and

updating the AML code with said appropriate update values.

2. (Original) The method of claim 1, wherein said pointer to a starting address of the AML code is stored in a Differentiated System Description Table.

3. (Currently Amended) The method of claim 2, further comprising [[:]] updating size of the Differentiated System Description Table.

4. (Currently Amended) The method of claim 3, further comprising [[:]] re-computing a checksum for the entire Differentiated System Description Table.

5. (Original) The method of claim 1, wherein said providing appropriate update values includes receiving a board stock keeping unit (SKU).

6. (Original) The method of claim 5, wherein said providing appropriate update values further includes determining the appropriate update values based on said board SKU.

7. (Original) The method of claim 1, wherein the board capabilities include suspend state parameters.

8. (Currently Amended) A method for managing interfaces and power, comprising:

searching for a pointer to a starting address of a power management machine code;

providing appropriate update values for the power management machine code corresponding to board capabilities determined by using General Purpose Input Output (GPIO) pins;

updating the power management machine code with said appropriate update values; and

enabling an operating system to manage power and resources.

9. (Original) The method of claim 8, wherein said power management machine code is an AML code.

10. (Original) The method of claim 9, wherein said pointer to a starting address of the AML code is stored in a Differentiated System Description Table.

11. (Original) The method of claim 8, wherein said enabling an operating system includes reporting the board capabilities to the operating system.

12. (Original) The method of claim 8, wherein said enabling an operating system includes sending commands based on the power management machine code.

13. (Original) The method of claim 12, further comprising:

passing control to the operating system.

14. (Currently Amended) A method for reporting and handling interfaces and power, comprising:

searching for a pointer to a starting address of a power handling and reporting machine code;

providing appropriate update values for the power handling and reporting machine code corresponding to board capabilities determined by using General Purpose Input Output (GPIO) pins;

updating the power handling and reporting machine code with said appropriate update values; and

enabling an operating system to manage power and resources by reporting the board capabilities to the operating system.

15. (Original) The method of claim 14, wherein said enabling an operating system further includes sending commands based on the power handling and reporting machine code.

16. (Original) The method of claim 14, wherein said power management machine code is an AML code.

17. (Original) The method of claim 16, wherein said pointer to a starting address of the AML code is stored in a Differentiated System Description Table.

18. (Currently Amended) A method for updating ACPI machine language (AML) code, comprising:

searching for a pointer to a starting address of the AML code in an ACPI table;
providing appropriate update values for the AML code corresponding to board capabilities determined by using General Purpose Input Output (GPIO) pins;

updating the AML code with said appropriate update values;
updating size of the ACPI table containing the pointer to the starting address of the AML code; and

re-computing a checksum for the ACPI table.

19. (Original) The method of claim 18, wherein said pointer to a starting address of the AML code is stored in a Differentiated System Description Table.

20. (Original) The method of claim 18, wherein said ACPI table includes a Differentiated System Description Table.

21. (Original) The method of claim 18, wherein said providing appropriate update values includes receiving a board stock keeping unit.

22. (Currently Amended) A computer readable medium containing executable instructions which, when executed in a processing system, causes the system to update ACPI machine language (AML) code, comprising:

searching for a pointer to a starting address of the AML code;

providing appropriate update values for the AML code corresponding to board capabilities determined by using General Purpose Input Output (GPIO) pins; and

updating the AML code with said appropriate update values.

23. (Original) The computer readable medium of claim 22, wherein said pointer to a starting address of the AML code is stored in a Differentiated System Description Table.

24. (Currently Amended) The computer readable medium of claim 23, further comprising [[:]] updating size of the Differentiated System Description Table.

25. (Currently Amended) The computer readable medium of claim 23, further comprising [[:]] re-computing a checksum for the entire Differentiated System Description Table.

26. (Currently Amended) An Advanced Configuration Power Interface (ACPI) system, comprising:

a pre-boot code to enable selection of ACPI capabilities according to a board parameter that defines board capabilities determined by using General Purpose Input Output (GPIO) pins;

a table to store pointers to ACPI machine language code; and

an ACPI machine language code update element to update the ACPI machine language code corresponding to the board capabilities.

27. (Original) The system of claim 26, wherein said board parameter includes board's stock keeping unit.

28. (Original) The system of claim 26, wherein the table includes Differentiated System Description Table.

29. (Currently Amended) A method for managing interfaces and power, comprising:

searching for a pointer to device node structures;

providing appropriate update values for the device node structures corresponding to board capabilities determined by using General Purpose Input Output (GPIO) pins; and

updating the device node structures with said appropriate update values.

30. (Original) The method of claim 29, wherein said updating the device node structures includes updating the device node structures after reading General Purpose Input (GPI) values.

31. (Currently Amended) The method of claim 29, further comprising [[:]] enabling an operating system to manage power and resources.